WS100:

Safety Critical FPGA Design

By

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Abstract:

With the increased design complexity, faster and bigger FPGA are necessary. Designers are seeing more complexity; more project delay and goes over budget because these FPGA designs are larger and more complex. Mentor has examined these challenges from every angle and determined where optimizing and solving challenges of the FPGA design process can greatly reduce your time to market. We shall present what Mentor offers as a comprehensive FPGA solution, starting from requirements driven development that provides continuity from system specifications into the hardware flow, including hardware definition, RTL coding through verification and synthesis.

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